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 Circuits and Systems, 2000. Proceedings. ISCAS 2000 Geneva. The 2000 IEEE International Symposium on , Volume: 4 , 28-31 May 2000  
 Pages:217 - 220 vol.4

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#### 2 ATPRG: an automatic test program generator using HDL-A for fault diagnosis of analog/mixed-signal integrated circuits

Wei-Hsing Huang; Chin-Long Wey;

 Instrumentation and Measurement, IEEE Transactions on , Volume: 47 , Issue: 2 , April 1998  
 Pages:426 - 431

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#### 3 A design, simulation and synthesis tool for delta-sigma-modulator-signal sources

Haurie, X.; Roberts, G.W.;

 Circuits and Systems, 1996. ISCAS '96., 'Connecting the World'. , 1996 IEEE International Symposium on , Volume: 4 , 12-15 May 1996  
 Pages:715 - 718 vol.4

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